# Silicon N Channel MOS FET High Speed Power Switching

# HITACHI

ADE-208-727 (Z) 1st. Edition January 1999

#### **Features**

· Low on-resistance

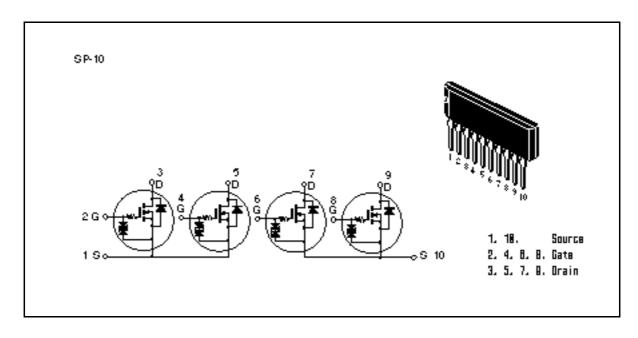
 $N~Channel~:~R_{\text{DS(on)}}~~0.5$  ,  $~V_{\text{GS}} = 10V,\,I_{\text{D}} = 2.5A$ 

 $R_{\rm DS(on)} - 0.6$  ,  $V_{\scriptscriptstyle GS} = 4V,\, I_{\scriptscriptstyle D} = 2.5A$ 

• 4V gate drive devices.

• High density mounting

#### **Outline**





## Absolute Maximum Ratings ( $Ta = 25^{\circ}C$ )

Item	Symbol	Ratings	Unit
Drain to source voltage	V <sub>DSS</sub>	120	V
Gate to source voltage	V <sub>GSS</sub>	±20	V
Drain current	I <sub>D</sub>	5	A
Drain peak current	Note1 D(pulse)	10	A
Body-drain diode reverse drain current	I <sub>DR</sub>	5	A
Channel dissipation	Pch(Tc=25°C) Note2	28	W
Channel dissipation	Pch Note2	3.5	W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Note: 1. PW 10 µs, duty cycle 1 %

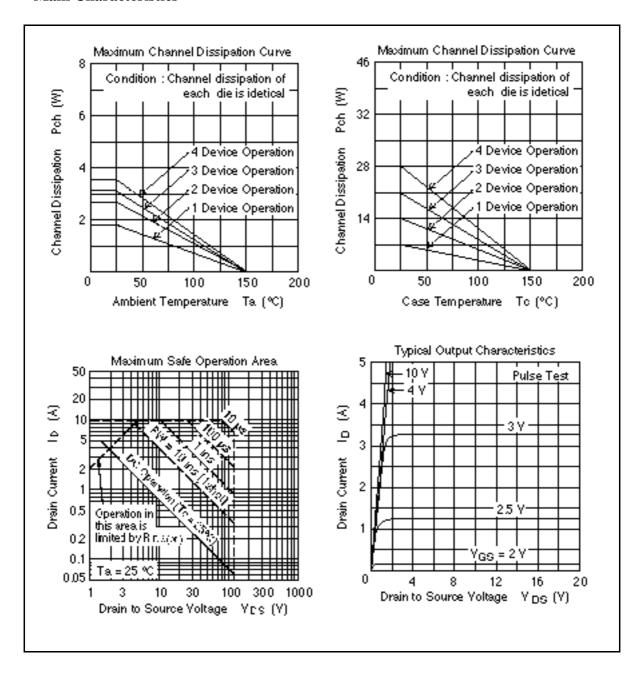
2. 4 devices poeration

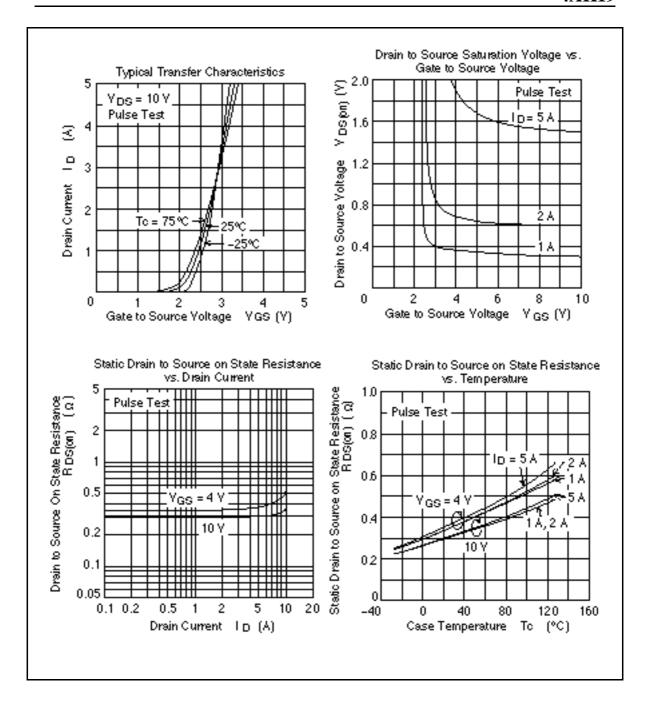
## **Electrical Characteristics** ( $Ta = 25^{\circ}C$ )

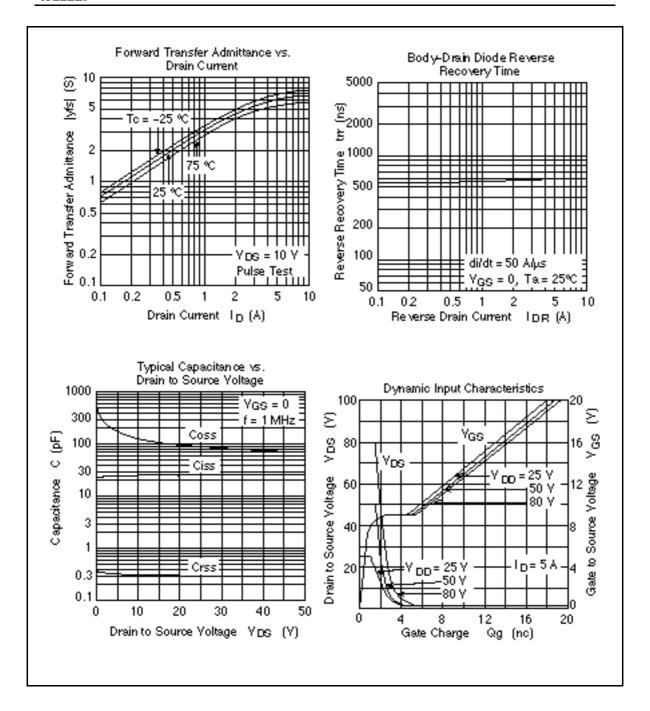
Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	120	_	_	V	$I_D = 10 \text{mA}, V_{GS} = 0$
Gate to source breakdown voltage	$V_{(BR)GSS}$	±20	_	_	V	$I_{G} = \pm 100 \mu A, V_{DS} = 0$
Zero gate voltege drain current	I <sub>DSS</sub>	_	_	100	μΑ	$V_{DS} = 100 \text{ V}, V_{GS} = 0$
Gate to source leak current	I <sub>GSS</sub>	_	_	±10	μΑ	$V_{GS} = \pm 16V, V_{DS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	1.0	_	2.0	V	$I_{D} = 1 \text{mA}, V_{DS} = 10 \text{V}$
Static drain to source on state resistance	$R_{\mathrm{DS(on)}}$	_	0.3	0.5		$I_D = 2.5A, V_{GS} = 10V^{Note3}$
Static drain to source on state resistance	$R_{\mathrm{DS(on)}}$	_	0.35	0.6		$I_D = 2.5A, V_{GS} = 4V^{Note3}$
Forward transfer admittance	y <sub>fs</sub>	3	5	_	S	$I_D = 2.5A, V_{DS} = 10V^{Note3}$
Input capacitance	Ciss	_	25	_	pF	V <sub>DS</sub> = 10V
Output capacitance	Coss	_	140	_	pF	$V_{GS} = 0$
Reverse transfer capacitance	Crss	_	3	_	pF	f = 1MHz
Gate series resistance	Rg	_	2.5	_	k	$V_{DS} = 0$ , $V_{GS} = 0$ , $f = 1MHz$
Turn-on delay time	t <sub>d(on)</sub>	_	0.3	_	μs	$V_{GS} = 10V, I_{D} = 2.5A$
Rise time	t <sub>r</sub>	_	0.45	_	μs	$R_{L} = 12$
Turn-off delay time	$t_{d(off)}$	_	6.6	_	μs	_
Fall time	t <sub>f</sub>	_	1.4	_	μs	_
Body-drain diode forward voltage	$V_{DF}$	_	1.1	_	V	$I_F = 5A, V_{GS} = 0$
Body-drain diode reverse recovery time	t <sub>rr</sub>	_	600	_	ns	$I_F = 5A, V_{GS} = 0$ diF/ dt =50A/µs

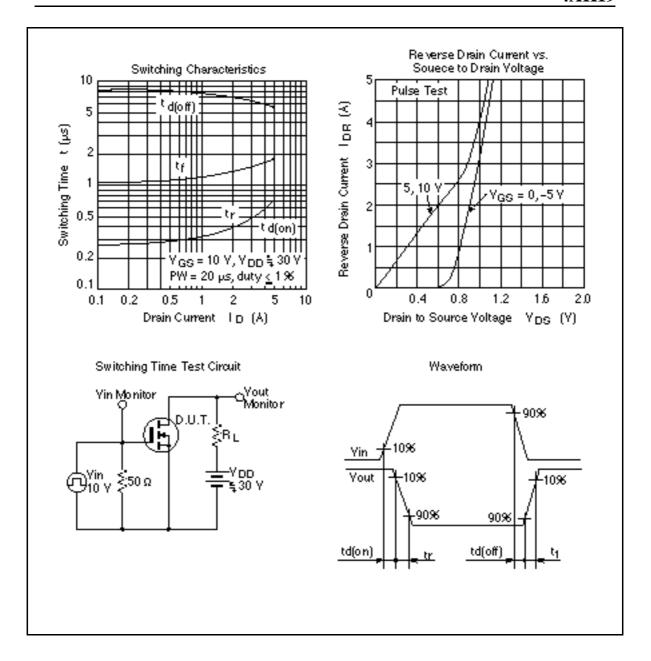
Note: 3. Pulse test

#### **Main Characteristics**



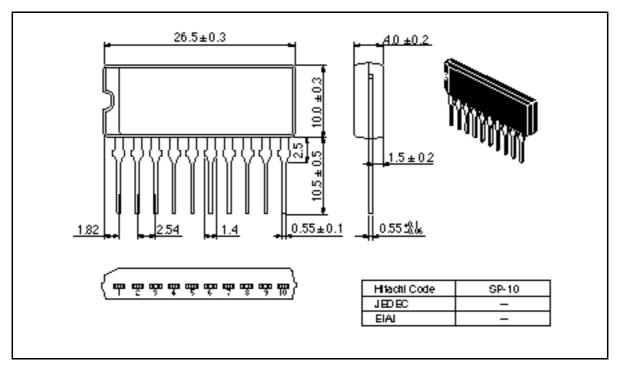






## **Package Dimensions**

Unit: mm



#### **Cautions**

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